PLAs

→ Programmable Logic Array.

$$f = \sum (\Pi \, x_i)$$

IN ⟶ [▷○ ▷○ ▷○] ── [PLANO AND] ── [PLANO OR] ⟶ out
x_i f

$\sum$

B —

A —

→ pseudo-nMOS

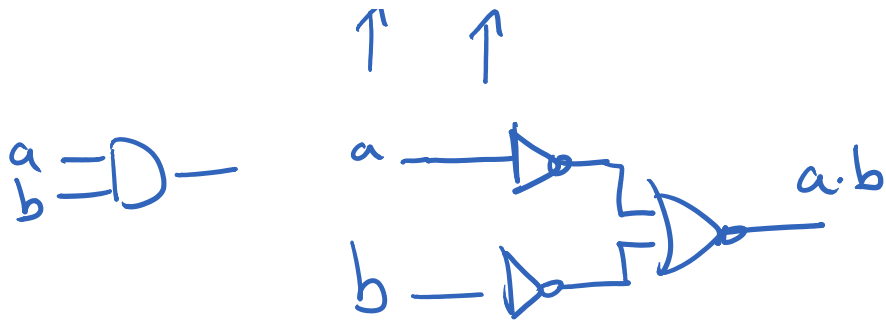$$a \cdot b = \overline{\bar{a} + \bar{b}}$$

a —⫸D—

a —▷o— —▷o— $a \cdot b$
b —▷o— b

$\boxed{\text{PLA}}$

$f = \sum \prod_{x_i}$

PLANO AND PLANO OR

in (x_i) out.

V_{DD}

$\overline{A + \overline{B} + \overline{C}} = \overline{A}BC$

AC

A B C

$S = AC + \overline{A}BC$